

Freeform Search

Database:	<input type="checkbox"/> US Pre-Grant Publication Full-Text Database <input checked="" type="checkbox"/> US Patents Full-Text Database <input type="checkbox"/> US OCR Full-Text Database <input type="checkbox"/> EPO Abstracts Database <input type="checkbox"/> JPO Abstracts Database <input type="checkbox"/> Derwent World Patents Index <input type="checkbox"/> IBM Technical Disclosure Bulletins
Term:	<input type="text" value="priorit\$7 near12 thread\$3 near9 bit\$1"/> <input type="button" value="Submit"/> <input type="checkbox"/>
Display:	<input type="text" value="20"/> Documents in <u>Display Format:</u> <input type="text" value="T1"/> Starting with Number <input type="text" value="1"/>
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search History

DATE: Wednesday, June 16, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u>	<u>Hit</u>	<u>Set</u>
<u>Name</u>	<u>Count</u>	<u>Name</u>
side by side		result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L25</u> priorit\$7 near12 thread\$3 near9 bit\$1	48	<u>L25</u>
<u>L24</u> priorit\$7 near12 thread\$3	1326	<u>L24</u>
<u>L23</u> L22 not l21	15	<u>L23</u>
<u>L22</u> lock\$3 near10 (flag\$1 or bit\$1) near20 thread\$3 near15 read\$5	17	<u>L22</u>
<u>L21</u> lock\$3 near5 (flag\$1 or bit\$1) near15 thread\$3 near15 ready	2	<u>L21</u>
<u>L20</u> (lock\$3 or synchroniz\$6 or enabl\$6 or activat\$4 or wait\$4 or inhibit\$3 or suspend\$3 or suspension or switch\$6) near5 bit\$1 near15 thread\$3 near9 ready	4	<u>L20</u>
<u>L19</u> L18 not l16 (lock\$3 or synchroniz\$6 or enabl\$6 or activat\$4 or wait\$4 or inhibit\$3 or suspend\$3 or suspension or switch\$6) near5 bit\$1 near15 thread\$3 near5 (plur\$7 or multiple or dual or two or second or child or nested)	7	<u>L19</u>
<u>L18</u> L16 not l14 (lock\$3 or synchroniz\$6 or enabl\$6 or activat\$4 or wait\$4 or inhibit\$3 or suspend\$3 or suspension) near5 bit\$1 near15 thread\$3 near5 (plur\$7 or multiple or dual or two or second or child or nested)	55	<u>L18</u>
<u>L17</u> L16 not l14 (lock\$3 or synchroniz\$6 or enabl\$6 or activat\$4 or wait\$4 or inhibit\$3 or suspend\$3 or suspension) near5 bit\$1 near15 thread\$3 near5 (plur\$7 or multiple or dual or two or second or child or nested)	11	<u>L17</u>
<u>L16</u> L14 not l13	48	<u>L16</u>
	8	<u>L15</u>

<u>L14</u>	(lock\$3 or synchroniz\$6) near5 bit\$1 near15 thread\$3 near5 (plur\$7 or multiple or dual or two or second or child or nested)	37	<u>L14</u>
<u>L13</u>	lock\$3 near4 bit\$1 near15 thread\$3 near5 (plur\$7 or multiple or dual or two or second or child or nested)	29	<u>L13</u>
<u>L12</u>	L11 and thread\$3	1	<u>L12</u>
<u>L11</u>	bit near1 wise\$1 near6 logic\$3 near7 programmable	6	<u>L11</u>
<u>L10</u>	bit near1 wise\$1 near6 logic\$3	427	<u>L10</u>
<u>L9</u>	(programmable or program\$4) near9 (mux or multiplex\$3 or sel or select\$3) near15 bit near3 wise	1	<u>L9</u>
<u>L8</u>	(programmable or program\$4) near5 (mux or multiplex\$3 or sel or select\$3) near15 bit near3 wise	1	<u>L8</u>
<u>L7</u>	(programmable) near5 (mux or multiplex\$3 or sel or select\$3) near7 bit near3 wise	0	<u>L7</u>
<u>L6</u>	l1 near25 bit near1 wise	4	<u>L6</u>
<u>L5</u>	L1 near15 thread\$1	15	<u>L5</u>
<u>L4</u>	L2 and (programmable) near5 (mux or multiplex\$3 or sel or select\$3) and bit near3 wise	1	<u>L4</u>
<u>L3</u>	L2 and (programmable) near5 (mux or multiplex\$3 or sel or select\$3) near15 bit near3 wise	0	<u>L3</u>
<u>L2</u>	L1 and thread\$1	379	<u>L2</u>
<u>L1</u>	(read or write) near6 register\$1 near9 only	7369	<u>L1</u>

END OF SEARCH HISTORY